

深圳市赛克斯康科技有限公司 SHENZHEN 6COM TECHNOLOGY CO.,LTD OPTICAL NETWORK TRANSCEIVER MANUFACTURER

6C-EPON-OUN-SFP

6COM,EPON-ONU,1.25Gb/s,1490nm,SMF,20KM,DDM,SC connector, 0°C to +70°C

FEATURES

- Single fiber Bi-Directional transceiver with single mode SC receptacle
- 1310nm burst-mode 1.25Gbps transmitter with DFB laser
- ◆ 1490nm continuous-mode 1.25Gbps receiver with PIN-TIA
- ◆ Complies with IEEE Std 802.3ah[™] -2004 1000BASE-PX20
- Digital diagnostic interface compliant with SFF-8472 Rev 9.4,
- Digital Diagnostic Monitoring (DDM) with external calibration
- 3.3V Single power supply
 LVPECL interface logic level for data input
 CML interface logic level for data output
 Differential line input/output impedance 100 ohm
 LVTTL for burst signal input and signal detect output
- Complies with RoHS directive (2002/95/EC)
- Operating case temperature:
 Standard: 0 to +70°C

APPLICATIONS

Gigabit Ethernet Passive Optical Network (EPON) ONU

Description

The 6C-EPON-OUN-SFP Bi-Directional Transceiver is the high performance module for single fiber communications by using 1310nm 1.25Gbps burst mode transmitter and 1490nm 1.25Gbps continuous receiver. It is Optical Network Unit (ONU) for IEEE STD 802.3ah[™] -2004 1000BASE-PX20. The optical transceiver is compliant with the Small Form- Factor Pluggable (SFP) Multi-Source Agreement (MSA).

The transmitter section uses a 1310nm DFB laser diode with automatic power control (APC) function and temperature compensation circuitry to ensure stable extinction ratio over all operating temperature range, and full IEC825 and CDRH class 1 eye safety. The receiver has a hermetically packaged PIN-TIA (trans-impedance amplifier) pre-amplifier and a limiting amplifier with CML compatible differential outputs.



1. Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Notes
Storage Temperature	Tst	-40	+85	°C	-
Operating Case Temperature	Tc	0	70	°C	-
Operating Humidity	RH	5	90	%	Non-condensing
Input Voltage	-	GND	Vcc	V	-
Power Supply Voltage	Vcc-Vee	0	3.6	V	-

2. Recommended Operating Conditions

Parameter		Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Standard	Tc	0		+70	°C
Power Supply Voltage		Vcc	3.13	3.3	3.47	V
Power Supply Current		lcc	-	-	400	mA

3. Optical and Electrical Characteristics

	Parameter	Symbol	Min	Typical	Max	Unit	Notes	
		Trans	mitter					
Tx Data Rate		Rτ		1.25	-	Gb/S	-	
Ce	entre Wavelength	λς	1260	1310	1360	nm	-	
	Spectral Width	Δλ		-	3	nm	-	
Side Mo	de Suppression Ratio	SMSR	30	-	-	dB	-	
Aver	age Output Power	Pout	0	-	7	dBm	1	
E	Extinction Ratio	ER	9	-	-	dB	-	
Bu	rst Enable Delay	Ton		-	32	ns	Fig.1	
Bu	rst Disable Delay	Toff			32	ns	Fig.1	
Average	e Launch Power-OFF Transmitter	Poff			-41	dBm		
Optical Eye Diagram		Compliant with of IEEE 802.3ah (Fig. 60-6) transmitter eye mask definition						
Optical Rise/Fall Time (20%~80%)		tr/tf			26 0	ps		
Data Input Swing Differential		V _{IN}	200		1600	mV	2	
Input Differential Impedance		Z _{IN}	90	100	110	Ω		
Burst	Disable		2.0		Vcc	V		
	Enable		0		0.8	V		
TX Fault	Fault		2.0		Vcc	V		
	Normal		0		0.8	V		
		Rec	eiver					
	Rx Data Rate	R _R	-	1.25	-	Gb/s	3	
	entre Wavelength	λς	1480		1500	nm		
	ver Sensitivity(BOL)	Sen			-27	dBm	3	
Receiver Overload		Sat	-3			dBm	3	
Receiver Reflectance					-12	dB		
Signal Detect De-Assert		SDD	-44			dBm		
Signal Detect Assert		SDA			-30	dBm		
	Signal Detect Hysteresis		0.5		6	dB		
Output [Differential Impedance	Z _{IN}	90	100	110	Ω		
	tput Swing Differential	Vout	400		1000	mV		
SD Output	High		2.0		Vcc	V		



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[Voltage	Low	0	0.8	V	
No	tool					

Notes:

1. The optical power is launched into SMF.

2. PECL input, internally DC-coupled and terminated.

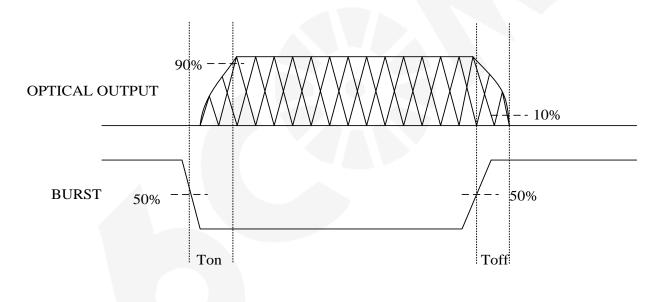
3. Measured with a PRBS 2^{7} -1 test pattern @1250Mbps, BER \leq 1×10⁻¹⁰.

4. Diagnostics

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to 70	С°С	±3°C	Internal / External
Voltage	3.0 to 3.6	V	±3%	Internal / External
Bias Current	0 to 100	mA	±10%	Internal / External
TX Power	0 to 4	dBm	±3dB	Internal / External
RX Power	-26 to -3	dBm	±3dB	Internal / External

5. Transmitter Burst Mode Timing Characteristics

Definition of Burst Enable Delay (Ton) and Burst Disable Delay (Toff)



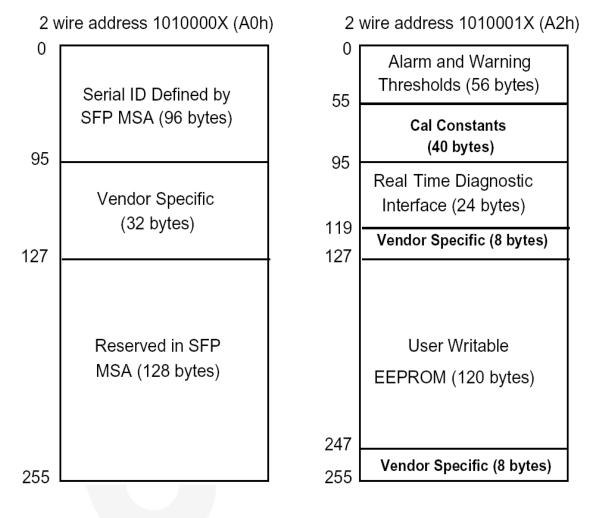


6. Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

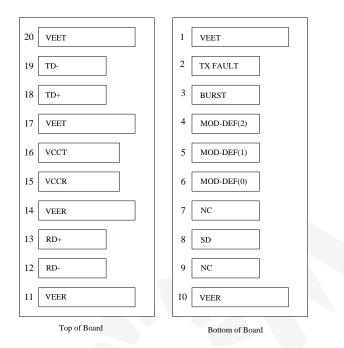
The digital diagnostic memory map specific data field defines as following.





7. Pin Definitions

Pin Diagram



Pin Descriptions

Pin	Signal Name	Description	Plug Seq.	Notes
1	VEET	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	BURST	Burst Single	3	Note 2
4	MOD_DEF(2)	SDA Serial Data Signal	3	Note 3
5	MOD_DEF(1)	SCL Serial Clock Signal	3	Note 3
6	MOD_DEF(0)	TTL Low	3	Note 3
7	-	-	3	
8	SD	Signal Detect Output	3	Note 4
9	-	-	3	
10	V _{EER}	Receiver ground	1	
11	VEER	Receiver ground	1	
12	RD-	Inv. Received Data CML Output, internal AC Coupling	3	Note 5
13	RD+	Received Data CML Output, internal AC Coupling	3	Note 5
14	V _{EER}	Receiver ground	1	
15	V _{CCR}	Receiver Power Supply	2	
16	V _{CCT}	Transmitter Power Supply	2	
17	V _{EET}	Transmitter Ground	1	
18	TD+	Transmit Data LVPECL Input, Internal DC Coupling	3	Note 6
19	TD-	Inv. Transmit Data LVPECL Input, Internal DC Coupling	3	Note 6
20	VEET	Transmitter Ground	1	

Notes:

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Plug Seq.: Pin engagement sequence during hot plugging.

- TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) BURST is a TTL input. When it is low, LD is on; when it is high, LD is off.
- Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be VccT or VccR.

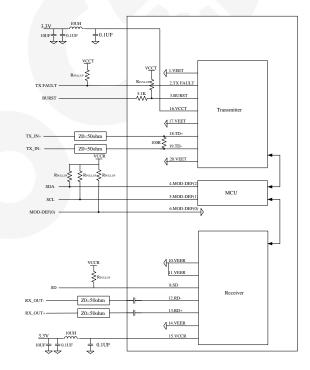
Mod-Def 0 is grounded by the module to indicate that the module is present

Mod-Def 1 is the clock line of two wire serial interface for serial ID

Mod-Def 2 is the data line of two wire serial interface for serial ID

- 4) SD is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor. Pull up voltage between 2.0V and Vcc+0.3V. Logic 0 indicates loss of signal; Logic 1 indicates normal operation. In the low state, the output will be pulled to less than 0.8V.
- 5) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 6) TD-/+: These are the differential transmitter inputs. They are internally DC-coupled, differential lines with 100Ω differential termination inside the module.

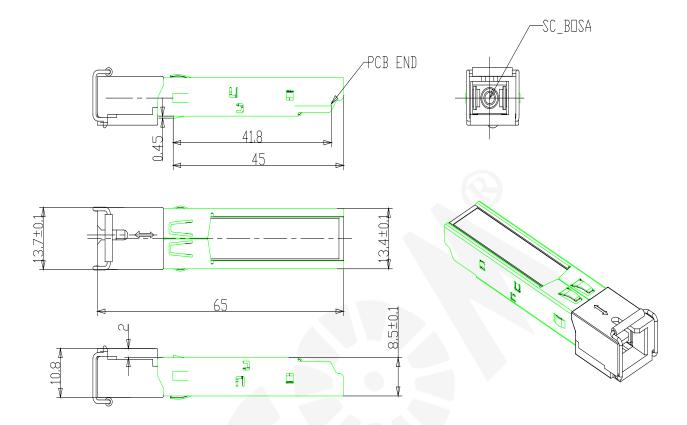
8. Recommend Application Circuit





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9 Mechanical Dimensions



10. Ordering information

Part Number	Product Description
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