

Features

- Supports up to 14.025Gbps bit rates
- Hot-pluggable SFP+ footprint
- 1310nm DFB laser and PIN photodiode, Up to 10km for SMF transmission
- Compliant with SFP+ MSA and SFF-8472 with duplex LC receptacle
- Built-in digital diagnostic functions
- Single +3.3V power supply
- Metal enclosure, for lower EMI
- ROHS compliant and lead-free
- Operating Temperature: Standard 0~70°C, Extended -10~85°C, Industrial -40~85°C

Applications

- 4G/8G/16G Fibre channel

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit	Notes
Maximum Supply Voltage	V _{cc}	-0.5	4.5	V	
Storage Temperature	T _s	-40	85	°C	
Operating Humidity	RH	5	85	%	

Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage	V _{cc}	3.13	3.3	3.47	V	
Power Supply Current	I _{cc}			300	mA	
Case Operating Temperature	T _c	0		70	°C	Standard
		-10		85	°C	Extended
		-40		85	°C	Industrial
Data Rate		4.25		14.025	Gbps	
Maximum supported distance				10	km	SMF

Optical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transmitter						
Centre Wavelength	λ_c	1270	1310	1350	nm	
Spectral Width (RMS)	$\Delta\lambda$			1	nm	



Side-Mode Suppression Ratio	SMSR	30	-		dB	
Average Output Power	P _{out}	-5		-0.5	dBm	1
Extinction Ratio	ER	3.5			dB	
Data Input Swing Differential	V _{IN}	180		850	mV	2
Input Differential Impedance	Z _{IN}	90	100	110	Ω	
TX Disable	Disable	2.0		V _{cc}	V	
	Enable	0		0.8	V	
TX Fault	Fault	2.0		V _{cc}	V	
	Normal	0		0.8	V	
Receiver						
Centre Wavelength	λ _c	1260		1600	nm	
Receiver Sensitivity				-12	dBm	3
Receiver Overload		0.5			dBm	3
LOS De-Assert	LOS _D			-13	dBm	
LOS Assert	LOS _A	-30			dBm	
LOS Hysteresis		0.5			dB	
Data Output Swing Differential	V _{out}	300		900	mV	4
LOS	High	2.0		V _{cc}	V	
	Low			0.8	V	

Note :

1. The optical power is launched into SMF.
2. PECL input, internally AC-coupled and terminated.
3. Measured with a PRBS 2³¹-1 test pattern @10312Mbps, BER ≤1×10⁻¹².
4. Internally AC-coupled.

Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t _{on}			1	ms
Tx Disable Assert Time	t _{off}			10	μs
Time To Initialize, including Reset of Tx Fault	t _{init}			300	ms
Tx Fault Assert Time	t _{fault}			100	μs
Tx Disable To Reset	t _{reset}	10			μs



LOS Assert Time	t_loss_on			100	µs
LOS De-assert Time	t_loss_off			100	µs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V _H	2		V _{cc}	V
MOD_DEF (0:2)-Low	V _L			0.8	V

Diagnostics

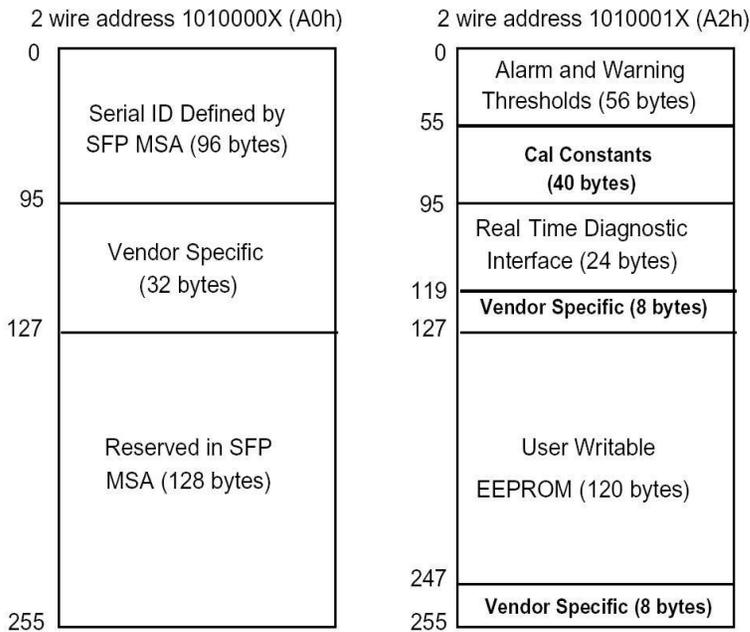
Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
	-10 to +85			
	-40 to +85			
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 15	mA	±10%	Internal
TX Power	-6.0 to -0.5	dBm	±3dB	Internal
RX Power	-16 to -1	dBm	±3dB	Internal

Digital Diagnostic Memory Map

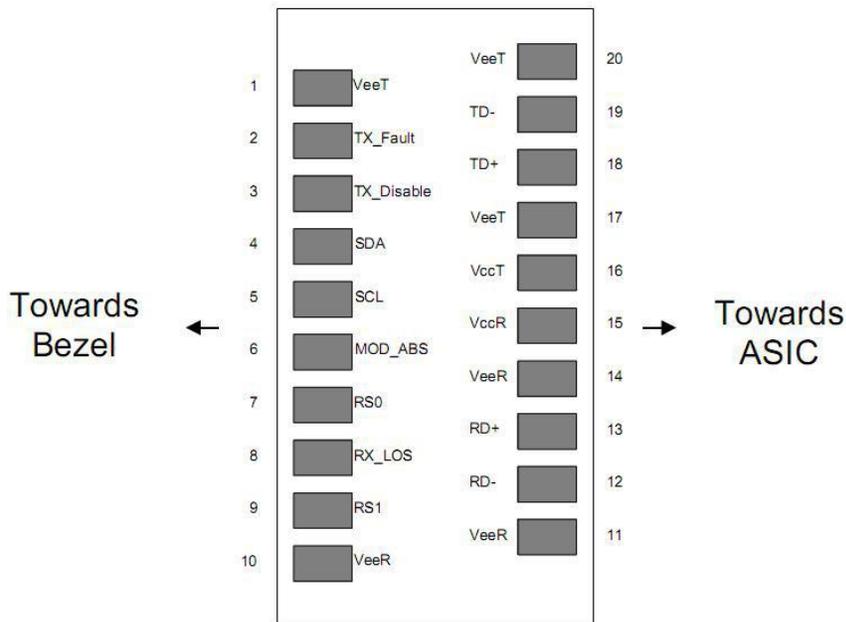
The 16G Fibre Channel SFP+ LW transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



Pin Definitions



Pin Descriptions

Pin	Signal Name	Description	Plug Seq.	Notes
1	V _{EET}	Transmitter Ground	1	
2	TX_FAULT	Transmitter Fault Indication	3	Note 1
3	TX_DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	



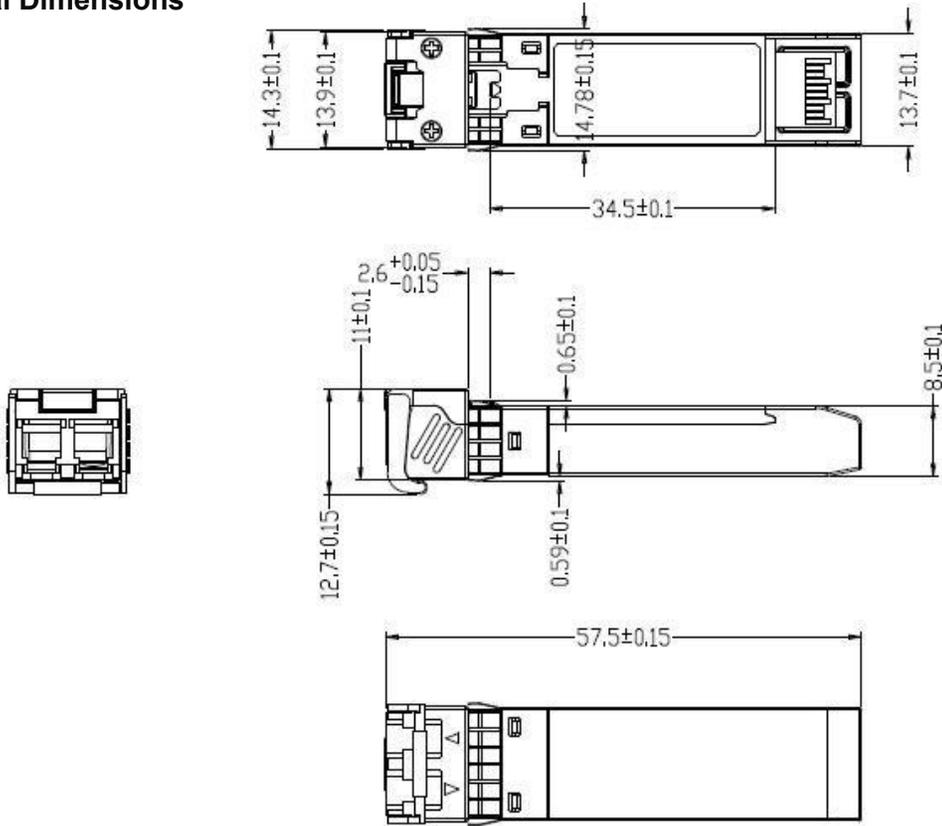
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V _{EER}	Receiver ground	1	
11	V _{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	V _{EER}	Receiver ground	1	
15	V _{CCR}	Receiver Power Supply	2	
16	V _{CCT}	Transmitter Power Supply	2	
17	V _{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	V _{EET}	Transmitter Ground	1	

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

1. TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
2. Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
3. LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
4. RD-/+ : These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
5. TD-/+ : These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

Mechanical Dimensions



Ordering information

Part number	Description
QT-SFP+-16G-LR	16G FC SFP+ LW transceiver, SMF, 1310nm, 10km, LC, DOM, 0~70°C